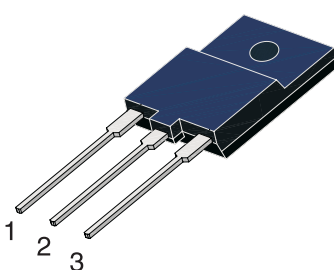
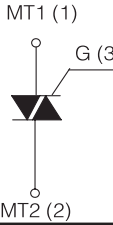


HIGH COMMUTATION TRIAC

<p>TO-3P FP</p>  	<table border="1"> <tr> <td>On-State Current 40 Amp</td><td>Gate Trigger Current ≤ 50 mA (16)</td></tr> <tr> <td colspan="2">Off-Satate Voltage 600 V ÷ 800V</td></tr> </table> <p>FEATURES</p> <ul style="list-style-type: none"> • Glass/passivated die junctions • High current Triac • Low thermal resistance with clip bonding • High commutation • High surge current capability • Low forward voltage drop • Solder dip 260 °C, 10s • Component in accordance to RoHS 2011/65/EU and WEEE 2002/96/EC • Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C <p>MECHANICAL DATA</p> <ul style="list-style-type: none"> • Case: TO-3P FP. Epoxy meets UL 94V-0 flammability rating. • Polarity: As marked on the body. • Terminals: Matte tin plated leads, solderable per MIL-STD-750 Method 2026, J-STD-002 and JESD22-B102. Consumer grade, meets JESD 201 class 1A whisker test. <p>TYPICAL APPLICATIONS</p> <p>Used on inductive loads, thanks to their high commutation performances.</p>	On-State Current 40 Amp	Gate Trigger Current ≤ 50 mA (16)	Off-Satate Voltage 600 V ÷ 800V	
On-State Current 40 Amp	Gate Trigger Current ≤ 50 mA (16)				
Off-Satate Voltage 600 V ÷ 800V					

Maximun Ratings and Electrical Characteristics at 25 °C

SYMBOL	PARAMETER	CONDITIONS	Value	Unit
$I_{T(RMS)}$	RMS On-State Current (full sine wave)	All Conduction Angle, $T_c = 80\text{ °C}$	40	A
I_{TSM}	Non-repetitive On-State Current	Full Cycle, 60 Hz ($t = 16.7\text{ ms}$)	420	A
I_{TSM}	Non-repetitive On-State Current	Full Cycle, 50 Hz ($t = 20\text{ ms}$)	400	A
I^2t	Fusing Current	$t_p = 10\text{ ms}$, Half Cycle	1000	A ² s
I_{GM}	Peak Gate Current	20 μ s max. $T_j = 125\text{ °C}$	8	A
$P_{G(AV)}$	Average Gate Power Dissipation	$T_j = 125\text{ °C}$	1	W
di/dt	Critical rate of rise of on-state current	$I_G = 2X\ I_{GT}$, $t_r \leq 100\text{ ns}$ $f = 120\text{ Hz}$, $T_j = 125\text{ °C}$	50	A/ μ s
T_j	Operating Temperature		(-40 to + 125)	°C
T_{stg}	Storage Temperature		(-40 to + 150)	°C
T_{sld}	Soldering Temperature	10s max.	260	°C
V_{iso}	RMS isolation voltage 50/60 Hz sinusoidal waveform		1.500	Vac

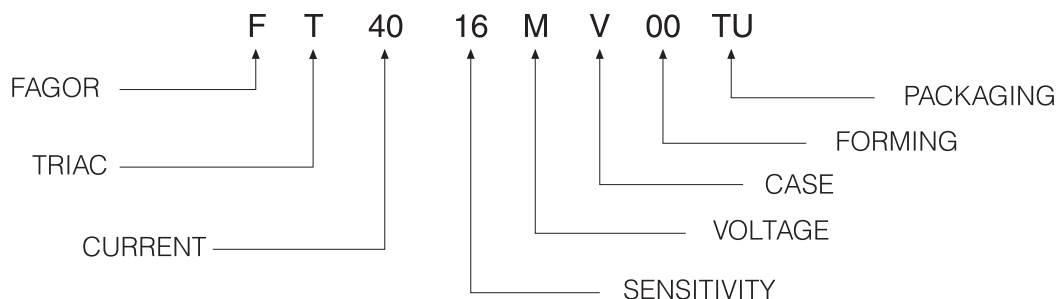
SYMBOL	PARAMETER	Voltage		Unit
		M	N	
V_{DRM} / V_{RRM}	Repetitive Peak Off State Voltage	600	800	V

Electrical Characteristics at Tamb = 25 °C

SYMBOL	PARAMETER	CONDITIONS	Quadrant		SENSITIVITY	Unit
					16	
$I_{GT}^{(1)}$	Gate Trigger Current	$V_D = 12 V_{DC}, R_L = 33\Omega, T_j = 25\text{ }^{\circ}\text{C}$	Q1÷Q3	MAX	50	mA
V_{GT}	Gate Trigger Voltage	$V_D = 12 V_{DC}, R_L = 33\Omega, T_j = 25\text{ }^{\circ}\text{C}$	Q1÷Q3	MAX	1.5	V
V_{GD}	Gate Non Trigger Voltage	$V_D = V_{DRM}, R_L = 3.3k\Omega, T_j = 125\text{ }^{\circ}\text{C}$	Q1÷Q3	MIN	0.2	V
$I_H^{(2)}$	Holding Current	$I_T = 100\text{ mA, Gate open, } T_j = 25\text{ }^{\circ}\text{C}$		MAX	80	mA
I_L	Latching Current	$I_G = 1.2 I_{GT}, T_j = 25\text{ }^{\circ}\text{C}$	Q1, Q3	MAX	80	mA
			Q2	MAX	160	mA
$dV / dt^{(2)}$	Critical Rate of Voltage Rise	$V_D = 0.67 \times V_{DRM}, \text{ Gate open, } T_j = 125\text{ }^{\circ}\text{C}$		MIN	500	V/ μ s
$V_{TM}^{(2)}$	On-State Voltage	$I_{TM} = 60\text{ Amp, } tp = 380\text{ }\mu\text{s, } T_j = 25\text{ }^{\circ}\text{C}$		MAX	1.55	V
$V_{t(0)}^{(2)}$	Threshold Voltage	$T_j = 125\text{ }^{\circ}\text{C}$		MAX	0.85	V
$r_d^{(2)}$	Dynamic resistance	$T_j = 125\text{ }^{\circ}\text{C}$		MAX	10	m Ω
I_{DRM} / I_{RRM}	Off-State Leakage Current	$V_D = V_{DRM}, T_j = 125\text{ }^{\circ}\text{C}$		MAX	5	mA
		$V_R = V_{RRM}, T_j = 25\text{ }^{\circ}\text{C}$		MAX	20	μ A
$R_{th(j-c)}$	Thermal Resistance Junction-Case	for AC 360° conduction angle			1.1	$^{\circ}\text{C/W}$

(1) Minimum I_{GT} is guaranteed at 5% of $I_{GT\text{ max}}$.

(2) For either polarity of electrode MT2 voltage with reference to electrode MT1.

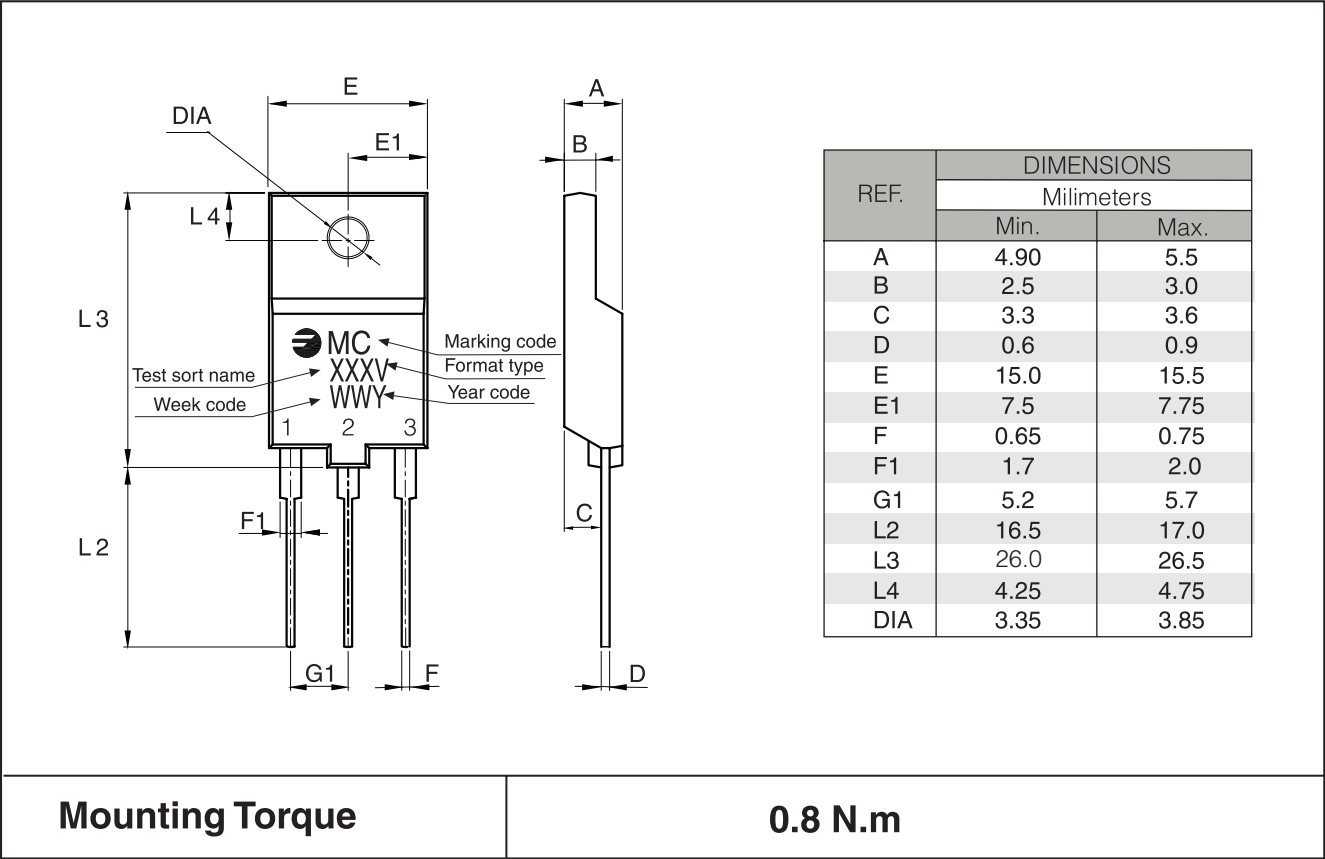
Part Number Information


HIGH COMMUTATION TRIAC

Ordering information

PREFERRED P/N	PACKAGE CODE	DELIVERY MODE	BASE QUANTITY	UNIT WEIGHT (g)
FT4016MV 00TUC	TU	TUBE	450	5.6

Package Outline Dimensions: (mm) TO-3P FP



Ratings and Characteristics (Ta 25 °C unless otherwise noted)

Fig. 1: Maximum power dissipation versus RMS on-state current (full cycle).

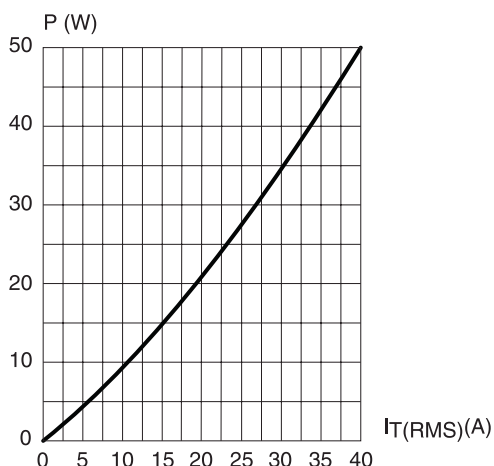


Fig. 2: RMS on-state current versus case temperature (full cycle).

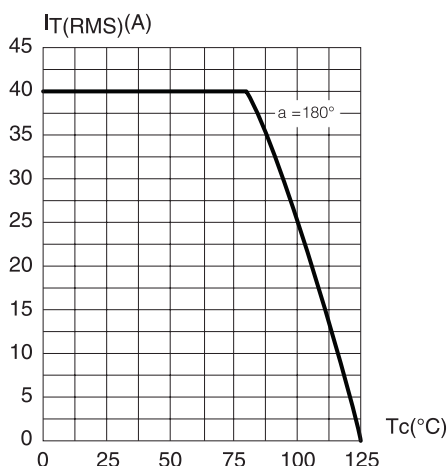


Fig. 3: On-state characteristics (maximum values)

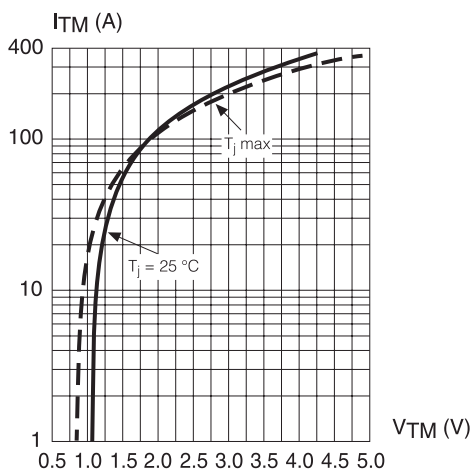


Fig. 4: Surge peak on-state current versus number of cycles

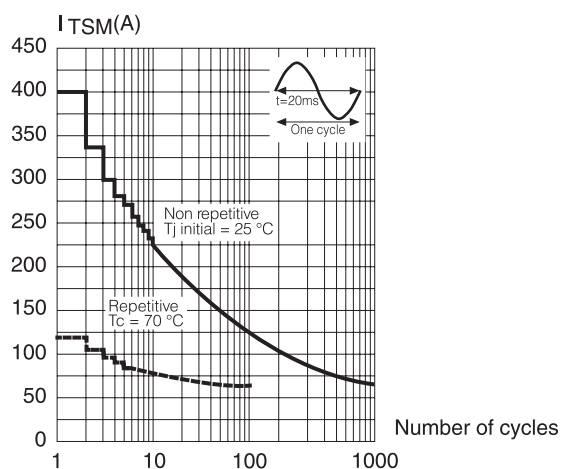


Fig. 5: Non repetitive surge peak on-state current for a sinusoidal pulse with width: $t_p < 10$ ms, and corresponding value of $I^2 t$.

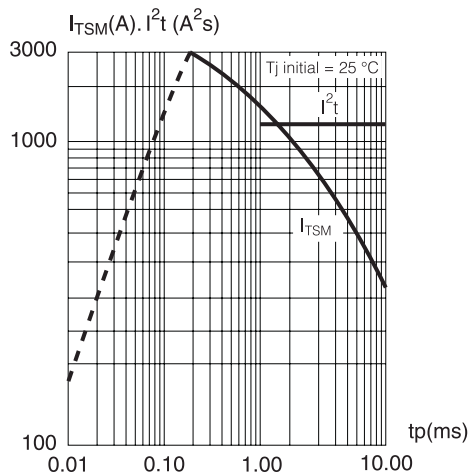
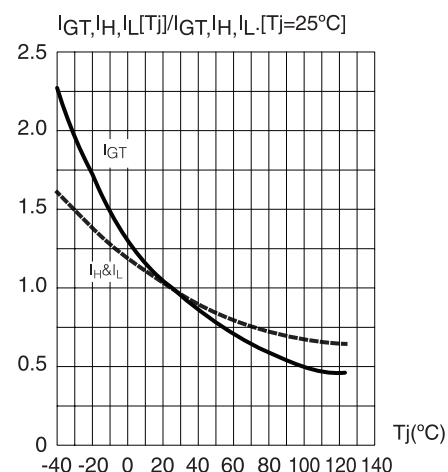


Fig. 7: Relative variation of gate trigger current, holding current and latching current versus junction temperature (typical values)



Revision History

DATE	REVISION	DESCRIPTION OF CHANGES
14-Apr-2015	0	Original Data Sheet
04-Jun-2015	1	Add V_{iso}

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